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Design and Analysis of PV Array with Multilevel Inverter

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ABSTRACT

In this work, a novel power conversion structure for grid-connected photovoltaic applications is presented. This structure is based on a Diode clamped multilevel inverter. The configuration of the PV system is based on the multi-string technology and the maximum power point is obtained using PSO Algorithm. The output of the MPPT tracker controls the duty cycle of the boost converter. To control this power converter, SVPWM based modulation technique is implemented. The proposed system operation was simulated using the MATLAB/Simulink power system toolbox and are furnished to verify the efficiency of this method.

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Introduction

The increase in industrialization leads to energy demand. Most of the energy demand is supplied by the fossil fuels. However, increase in air pollution, diminishing fossil fuels and their increasing cost have made it necessary to gaze towards renewable energy sources as a future energy solution. Among these Renewable Energy Sources (RES), solar power systems are the affable solution for electrification. As the solar energy is available in nature and due to its inexhaustible availability, it has become one of the most promising renewable energy. Hence, PV system has been increasingly used in medium sized grid. The increasing use of semiconductors in the high power applications lead to the development of multilevel inverters[6]. Multilevel inverters can operate at high switching frequencies with low harmonic distortion. So it is gained more attention in high power application .A multilevel inverter not only achieves high power ratings, but also enables the use of renewable energy sources. Renewable energy sources such as photovoltaic, wind and fuel cells, can be easily interfaced to a multilevel inverter system for high power applications [9]. Multi Level Inverter topologies have been widely classified into several topologies such as diode clamped multilevel inverter or neutral point clamped multilevel inverter, flying capacitor based multilevel inverter and cascaded multilevel inverter. Multilevel converters not only can generate the output voltages with very low distortion, but also can reduce the dv/dt stresses. Therefore electromagnetic compatibility (EMC) problems can be reduced. Apart from these advantages, their design and control is much more complex. Hence in order to overcome this drawbacks, this proposed a modeling and simulation of a multilevel inverter using Neutral Point-Clamped (NPC) inverters. The main aim of this work is to develop a cost effective, simple and efficient high performance inverter with low harmonics at its output.

Proposed Methodology

The Configurations of a proposed system is shown in Figure 1.



Figure 1. Block Diagram of a PV and grid

As the photovoltaic power varies with the climatic conditions, to obtain the maximum power from PV array, it is coupled with a Maximum Power Point Tracker (MPPT). As the photovoltaic energy sources generate power at variable low dc voltage, it requires power conditioning. For this purpose, DC-DC converter is used. The output obtained from the DC-DC converter is coupled to the inverter [7].

Modeling of PV Array

The building block of PV arrays is the solar cell. It is basically a p-n junction which directly converts light energy into electricity. The equivalent circuit of a PV cell is shown in Figure 2.



Figure 2. Equivalent circuit of a PV cell.

PV cells grouped in large unit forms a PV module and are connected in series-parallel configuration to form PV array. The mathematical model of a PV array is represented by the equation:

$$I = n_{p}I_{ph} - n_{p}I_{rs}[exp\left(\frac{q}{KTA}\right)*(V/n_{s})-1]$$
⁽¹⁾

where

Iph - photo current

- I output current of PV array
- V output voltage of PV array
- n_s number of cells in series
- n_p number of cells in parallel
- q charge of an electron
- K Boltzmann's constant [8.62 x 10-5 eV/K]
- A ideality factor of the p-n junction. It ranges between 1-5.
- T cell temperature (K)

 I_{rs} - cell reverse saturation current.

The cell reverse saturation current I_{rs} varies with temperature according to the following equation:

$$I_{rs} = I_{rr}[T/T_{r}]^{3} \exp((qE_{G})/KA[(1/T_{r})-1/T)]^{2}$$

Where T_r is the reference temperature, I_{rr} is the reverse saturation temperature at T_r and E_G is the band gap of the semiconductor used in the cell.

The photo current I_{ph} depends on the solar radiation and cell temperature as follows:

$$I_{ph} = [I_{scr} + K_i(T - T_r)]s/100$$
(3)

Where I_{scr} is the short-circuit current at reference temperature 2.52A], K_i is the short circuit current temperature coefficient and s is the solar radiation in mW/cm³. Thus the calculated PV power can be given as:

$$P = IV = n_{p}I_{ph}V[(q/KTA)*(V/n_{s})-1]$$
(4)

The operation of PV system varies according to the weather condition. Hence a dynamic tracking system is required to obtain maximum power from the PV array.

Maximum Power Point Tracker

A typical solar panel converts only 30 to 40 percent of the incident solar radiation into electrical energy. Hence in order to improve the efficiency of the solar panel Maximum Power Point Tracking technique is implemented [1-5].

Maximum Power Point Tracker frequently referred as MPPT is an electronic system that operates the PV modules to produce maximum power. MPPT varies the electrical operating point of the modules so that the modules are able to deliver maximum power.

The MPPT maximizes the power produced by the panels by controlling the voltage and current of PV system. The PV output current and voltage are measured using a current sensor at the PV output terminal. From the measured voltage and current value the output power of PV is calculated. This power extraction control is necessary because the Maximum Power Point (MPP) of a solar panel varies with the radiation and temperature.

MPPT Algorithm

Particle Swarm Optimization

The Particle Swarm Optimization (PSO) was first developed by Kennedy (1995). It is a population based optimization technique, motivated by biological concepts like swarming and flocking. The basic idea behind PSO emerged from the behavior observed among flocks of birds, schools of fish or swarms of bees. The salient features of PSO are its easy implementation, its high speed convergence and nonrequirement of gradient information. An extensive range of different optimization problems can be solved by this technique. PSO is initialized with the population, which is randomly generated and it always conducts a search in the population of particles. Every particle in the population represents a possible candidate solution (fitness) to the given problem. In a PSO system, the search towards optima is carried out in a multidimensional search space. The particles can move through the search space and make changes in its position by having the information such as (i) the distance between the current position of the particle and Phest, (ii) the distance between the current position of the particle and Ghest. Every particle memorizes its best solution in addition to its position achieved so far and is known as *Phest*, the Personal best. It also knows the best value along with its position found in the group among *Pbests*, known as Gbest, the Global best. The basic theory of PSO insists on accelerating each particle on the road to its Pbest and Gbest locations. Concept of particle position the modification in PSO is shown in Figure 3.



Figure 3. Concept of particle position modification in PSO where V_i^{Pbest} is the velocity based on *Pbest* and

 V_i^{Gbest} is the velocity based on Gbest.

In *n* dimensional search space, every particle in the population can be a potential solution to a given problem. The position of the i^{th} particle is then represented as $X_i = (x_{i1}, x_{i2}, \dots, x_{in})$. A particle *i* is moving through a search space with a velocity that corresponds to $V_i = (v_{i1}, v_{i2}, \dots, v_{in})$.

Let *Pbest* and *Gbest* of the *i*th particle be given as $Pbest_i = \left(x_{i1}^{Pbest}, x_{i2}^{Pbest}, \dots, x_{in}^{Pbest}\right)$ and $Gbest = \left(x_1^{Gbest}, x_2^{Gbest}, \dots, x_n^{Gbest}\right)$. In PSO, the velocity of the individual particle *i* is updated using the following equation (5).

$$V_i^{(t+1)} = \omega \times V_i^{(t)} + c_1 \times r_1 \times (Pbest_i - X_i^{(t)}) + c_2 \times r_2 \times (Gbest - X_i^{(t)})$$
(5)
where

 $V_{\cdot}^{(t)}$ velocity of the i^{th} particle at iteration t,

t pointer of iterations (generations),

 ω inertia weight factor, that is used to control the impact of the previous velocity over the new velocity, c_1 , c_2 acceleration coefficients, where c_1 and c_2 are positive constants called as coefficient of self-recognition component and coefficient of social component, r_1 , r_2 random numbers

equally spread within the range [0, 2],

 $X_{i}^{(t)}$ position of the particle i at iteration t,

 $Pbest_i$ best position of particle *i* until iteration *t*,

Gbest best position of the group until iteration t.

The predefined values of the acceleration coefficient c_1, c_2 and inertia weight ω are substituted in the equation (2.4) and random numbers r_1, r_2 are uniformly generated

within the limit [0, 2] in order to calculate the new velocity. The inertia weight ω is calculated by the equation (6)

$$\omega = \omega_{\max} - \frac{\omega_{\max} - \omega_{\min}}{Iter_{\max}} \times Iter$$
⁽⁶⁾

where

 $\omega_{\rm max}$ initial weight

 ω_{\min} final weight

*Iter*_{max} maximum iteration number

Iter current iteration number

Hence, a new velocity which is used to perform a shift in the current searching point in the direction of Pbest and Gbest is calculated. Each particle tries to move from the current position to the new one by using the modified velocity, which is given by the equation (7):

$$X_{i}^{(t+1)} = X_{i}^{(t)} + V_{i}^{(t+1)}$$
⁽⁷⁾

Finally, in PSO all the particles are trying to move about better positions. By the combined effort of the whole population, the best position (optimum solution) can be obtained.

The flowchart the PSO algorithm for MPPT is shown in Figure 4.

Thus, a maximum power point tracker achieves maximum power from the solar PV module. A non isolated DC-DC converter (step up/ step down) is implemented for conversion of this maximum power to the grid. The MPPT controller controls the output voltage of the DC-DC converter by regulating the PWM signals applied to the switch of the inverter unit.

Boost Converter

The DC-DC step-up/step down (Buck-Boost) converter circuit regulates the output voltage of the PV module. The step-up converter configuration with a low duty-cycle value allows the converter to operate with small power losses.

DC/AC Inverter

A DC/AC inverter is a device that converts electrical power from DC to AC. Multilevel inverters (MLI) are recently used in high power applications. Moreover, three different major multilevel converter structures such as cascaded H-bridges converter with separate dc sources, diode clamped (neutral-clamped), and flying capacitors (capacitor clamped) have been reported in the literature. Among these structures, diode clamp inverter has the capability to reduce the harmonic content and decrease the voltage or current ratings of the semiconductors.



Figure 4. Flowchart of the PSO algorithm Diode Clamped Multilevel Inverter

The general structure of the multilevel inverter is to synthesize a sinusoidal voltage from several levels of voltages typically obtained from capacitor voltage sources. The socalled "multilevel" starts from three levels. A three level inverter, also known as a "neutral-clamped" inverter, consists of two capacitor voltages in series and uses the center tap as the neutral. Each phase leg of the three-level inverter has two pairs of switching devices in series. The center of each device pair is clamped to the neutral through clamping diodes. The output obtained from a three-level inverter is a quasi-square wave output if fundamental frequency switching is used. Multilevel inverters are being considered for an increasing number of applications due to their high power capability associated with lower output harmonics and lower commutation losses[8].

The Neutral –point clamped inverter produce the staircase output voltage by utilizing DC capacitors into generating varied stages of DC voltages. In the event where m is the number of levels and (m-1) is the quantity of required capacitors on the DC bus, then the quantity power electronic switches at each stage is (2-m) and the amount of diodes at each stage is 2(m-2). Usually, the diode clamped multilevel inverters are designed by incorporating these specific values. Diode clamped multilevel inverters has the

advantage of having high efficiency for switching at fundamental frequency ,low cost and lesser components comparing other topologies like cascaded H-bridge and flying capacitor multilevel inverters.

Space Vector Modulation

The space vector concept, which is derived from the rotating field of induction motor, is used for modulating the inverter output voltage. In this modulation technique the three phase quantities can be transformed to their equivalent twophase quantity either in synchronously rotating frame (or) stationary frame. From these two-phase components, the reference vector magnitude can be found and used for modulating the inverter output. The process of obtaining the rotating space vector is explained in the following section, considering the stationary reference frame. Considering the stationary reference frame let the three-phase sinusoidal voltage component be,

Va = VmSinwt

Vb = VmSin($\omega t - 2\pi/3$)

 $Vc = VmSin(\omega t - 4\pi/3)$

When this three-phase voltage is applied to the AC machine it produces a rotating flux in the air gap of the AC machine. This rotating resultant flux can be represented as single rotating voltage vector. The magnitude and angle of the rotating vector can be found by means of Clark's Transformation as explained below in the stationary reference frame. To implement the space vector PWM, the voltage the stationary dq reference frame that consists of the horizontal (d) and vertical (q) axes as depicted in Figure 5.



Figure 5. The relationship of abc reference frame and stationary dq reference frame.

As described in Figure 6,the transformation is equivalent to an orthogonal projection of $[a \ b \ c]^t$ onto the twodimensional perpendicular to the vector $[1 \ 1 \ 1]^t$ (the equivalent d-q plane) in a three-dimensional coordinate system. As a result, six non-zero vectors and two zero vectors are possible. Six non-zero vectors (V₁-V₆) shape the axes of a hexagonal as depicted in Figure 6, and supplies power to the load. The angle between any adjacent two non-zero vectors is 60 degrees. Meanwhile, two zero vectors (V₀ and V₇) and are at the origin and apply zero voltage to the load. The eight vectors are called the basic space vectors and are denoted by (V₀, V₁, V₂, V₃, V₄, V₅, V₆, V₇). The same transformation can be applied to the desired output voltage to get the desired reference voltage vector, V_{ref} in the d-q plane. The objective of SVPWM technique is to approximate the reference voltage vector V_{ref} using the eight switching patterns. One simple method of approximation is to generate the average output of the inverter in a small period T to be the same as that of V_{ref} in the same period



Switching States

For 180° mode of operation, there exist six switching states and additionally two more states, which make all three switches of either upper arms or lower arms ON. To code these eight states in binary (one-zero representation), it is required to have three bits $(2^3 = 8)$. And also, as always upper and lower switches are commutated in complementary fashion, it is enough to represent the status of either upper or lower arm switches. In the following discussion, status of the upper bridge switches will be represented and the lower switches will it's complementary. Let "1" denote the switch is ON and "0" denote the switch in OFF. Table-1 gives the details of different phase and line voltages for the eight states.

Computed Results and Discussion

A NPC based multilevel inverter system with a boost converter was used in order to connect the photovoltaic system to the power grid. The simulation study is carried out in MATLAB environment to verify the proposed control approach. The output of the boost converter is shown in figure 7. Figure 8 and figure 9 shows the output voltage and current waveform of the proposed inverter.



Figure 7. Output voltage of the converter.

Tuble 11 5 witching patterns and output vectors.									
Voltage Vectors	Switching vectors			Line to neutral voltage			Line to Line Voltage		
	Α	B	С	Van	V _{bn}	V _{cn}	Vab	V _{bc}	V ₀
V_0	0	0	0	0	0	0	0	0	0
V_1	1	0	0	2/3	-1/3	-1/3	1	0	-1
V_2	1	1	0	1/3	1/3	-2/3	0	1	-1
V ₃	0	1	0	-1/3	2/3	-1/3	-1	1	0
V_4	0	1	1	-2/3	1/3	1/3	-1	0	1
V ₅	0	0	1	-1/3	1/3	2/3	0	-1	1
V ₆	1	0	1	1/3	-2/3	1/3	1	-1	0
V	1	1	1	0	0	0	0	0	0

Table 1. Switching patterns and output vectors.





The efficiency of the proposed system is analyzed in terms order of harmonics.

THD Analysis:

Figure 10 illustrate plots of the order of harmonics versus magnitude of grid voltage. With the proposed system, the order of harmonics is less than 5%. The Fast Fourier Transform (FFT) is used to measure the order of harmonics.



Figure 10. Harmonic spectrum of Grid voltage

From the Figure 10, it is concluded that the total harmonic distortion (THD) of the voltage is improved to 3.25% which is within the acceptable limit of IEEE 519

standard for voltage distortions in distribution system. Thus from the simulation results, it is evident that this method can be efficiently used for harmonics reduction along with injection of active power from solar system.

Conclusion

This proposed work discussed about the simulation of PV based NPC multilevel inverter with intermediate boost converter. The PV array output power delivered to the load can be maximized using PSO control algorithm. The boost converter is allowed to work in continuous mode and the switching sequence of multilevel inverter is obtained by a SVPWM technique. Total Harmonic Distortion analysis was performed. From the FFT analysis, it is observed that THD is less for the proposed technique and therefore the proposed multilevel inverter is a suitable topology for photovoltaic applications.

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